

PATENT
Atty. Dkt. No. WHLK/043

IN THE CLAIMS

This listing of claims will replace all prior versions, and listings, of claims in the application:

1. (Original) An alarm unit, comprising:

a flash circuit having a flashtube for generating a flash; and

an application specific integrated circuit (ASIC) coupled to said flash circuit, for triggering said flash.

2. (Original) The alarm unit of claim 1, further comprising:

a switch, coupled to said ASIC, where said switch having a plurality of selectable positions representative of a plurality of intensity settings, wherein said flash has an intensity that is in accordance with a selected position of said switch.

3. (Original) The alarm unit of claim 2, wherein said plurality of intensity settings comprise four intensity settings.

4. (Original) The alarm unit of claim 1, further comprising:

a current limiting circuit, coupled to said ASIC, where said current limiting circuit limits an input current level.

5. (Original) The alarm unit of claim 4, wherein said current limiting circuit continuously senses said input current level.

6. (Original) The alarm unit of claim 1, further comprising:

a DC to DC converter, coupled to said ASIC, where said DC to DC boost converter provides over voltage protection.

7. (Original) The alarm unit of claim 1, wherein said ASIC is deployed in an eighteen-pin package.

PATENT

Atty. Dkt. No. WHLK/043

8. (Original) The alarm unit of claim 1, wherein said ASIC is deployed in a sixteen-pin package.

9. (Original) The alarm unit of claim 1, wherein said ASIC is deployed in an eight-pin package.

10. (Original) The alarm unit of claim 1, wherein said flash circuit further comprises a voltage doubler.

11. (Original) The alarm unit of claim 1, wherein said ASIC provides a charge cycle that is greater than 8 kilohertz.

12. (Original) The alarm unit of claim 1, further comprising:

an audio circuit, coupled to said ASIC, where said audio circuit generates an audio warning signal.

13. (Original) The alarm unit of claim 11, wherein said ASIC selects an audio frequency for said audio warning signal.

14. (Original) The alarm unit of claim 1, further comprising:

a synchronization detection circuit, coupled to said ASIC, where said synchronization detection circuit receives a synchronization signal to trigger said flash.

15. (Original) The alarm unit of claim 1, wherein said ASIC provides a transistor drive capability of greater than 7.3 volts.

16. (Currently amended) An alarm unit, comprising:

an audio circuit for generating an audio warning signal; and

an application specific integrated circuit (ASIC) coupled to said audio circuit, for triggering said audio warning signal, wherein said ASIC selects an audio frequency for said audio warning signal.

PATENT
Atty. Dkt. No. WHLK/043

17. (Canceled)